

UNIVERSITY OF PUNE



Examination Circular No. 50 of 2012

Programme of M.Sc. Electronic Science Sem. I to IV Examinations, November, 2012

1. Candidates are required to be present at the respective place of examination fifteen minutes before the time fixed for the first paper and ten minutes before the time of each subsequent paper.
2. Candidates are forbidden from taking any material into the examination hall, that can be used for malpractice at the time of examination.
3. Candidates are requested to see the Notice Board at their place of examination regularly for changes if any, that may be notified later in the programme.
4. No request for any special concession such as a change in time or any day fixed for the University Examination, on religious or any other ground shall be granted.
5. The Use of non-programmable Calculator is permitted.
6. The Examination of Candidates of University Post-Graduate Dept. having credit system will be held at their respective departments.
7. The Centers and place of the examination will be as follows :

Sr. No.	Centre Code	College Code	Name of the Centres
1.	001	006	N. Wadia College of Arts & Science, Pune-411 001.
2.	016	032	T.C.College, Baramati
3.	006	077	New Arts, Com. & Sci. College, Ahmednagar
4.	.021	042	S.S.G.M. College, Kopergaon
5.	003	062	K.T.H.M. College, Nasik.
6.	026	052	Arts Science & Commerce College, Satana

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8. The written examination will be conducted in the following order :

Day & Date	SUBJECT SEMESTER-II Time : 10.30 A.M. TO 1.30 P.M.	SUBJECT SEMESTER-IV Time : 2..30 P.M. TO 5.30 P.M.
Tuesday, 20.11.2012	EL-2UT-04 :Applied Electromagnetics RFA Microwave	EL4UT 06 : Control System: Theory & Application
Wednesday, 21.11.2012	EL-2UT-05 : Communication Electronics	-----
Thursday, 22.11.2012	EL-2 UT-06 : Digital System Design using VHDL	-----
Day & Date	SUBJECT SEMESTER-I Time : 10.30 A.M. TO 1.30 P.M.	SUBJECT SEMESTER-III Time : 2 .30 P.M. TO 5. 30 P.M.
Friday, 23.11.2012	EL1UT-01 : Foundation of Semiconductor Devices	EL3UT -05 :Embedded Systems
Saturday, 24.11.2012	EL1UT-02 : Analog circuits : Design & Analysis	-----
Monday, 26.11.2012	EL1UOT-01: Network Analysis and Synthesis(Old) OR EL1UOT-02: Opto-Electronics(Old) EL1UOT-03: Instrumentation & measurement Techniques (New)	-----

9.	The Practical Examination will be conducted before theory examination between 29.10.2012 & 10.11.2012 Due notice about the place, date and time of the Practical Examination will be displayed on the Notice-Board. Candidates are directed to be present at the time and place of their practical examination.
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Ganeshkhind,
Pune-411 007
Ref. No.XCT/Sci./ 1129
Date:25.10.2012.

Dr. (Smt.)Sampada Joshi
Controller of Examinations