

M.E. Electrical / E & TC /
Electronics / Metallurgy

October/April 20 .

Seat No.



University of Pune

EXAMINATION FOR DEGREE IN MASTER OF ENGINEERING (REVISED 2002 COURSE)

EXAMINATION FEE

Rs. 1440/- PER SEMESTER + Exam. Form Rs. 30/-

To,

THE CONTROLLER OF EXAMINATIONS,
UNIVERSITY OF PUNE,
Pune-411007.

Sir,

I request permission to present myself for the Examination in Master of Engineering ()
Revised 1994 Course to be held in October/April, 200 , and pay herewith the prescribed Fee Rs. ()
I desire to offer the undermentioned papers for Sem. (I/II/III) Examination.

I am submitting Dissertation of the Topic

Yours faithfully,

Date :

Signature

Branch :

College :

Male

Centre :

Female

Name :

(in Capital Letter)

Surname

First Name

Father's/Husband's Name

.....
Mother's Name

South Indians/Other should enter the Name in Usual Form

Name in Devnagari Script :

Date of Registration as Post-graduate Students :

Particulars of that Registration :

Date of Passing B.E. Examination :

Year and Month

Branch

Seat No.

University

Date of Obtaining B.E. Degree :

Last Appearance at M.E. Examination :

Month

Year

Seat No.

(Copy of last appearance of M.E. Examination should be enclosed).

CERTIFICATE BY RECOGNISED TEACHER / GUIDE

I certify that Shri./Smt.
has worked under my direction for two/four academic terms from to
in College / Institute / Department and that the Dissertation
on a synopsis of which has been signed by me is entirely
the work of the candidate and has been approved by the University.

Signature

.....
(Signature of the Guide)

Designation

CERTIFICATE BY THE HEAD OF THE INSTITUTION / COLLEGE

I certify that Shri./Smt.
has satisfactorily attended a course of lectures for each of papers. He/She has my
permission to appear for the Examination.

Signature

Designation

Address for Correspondence :
.....

Semester I		M.E. (Electrical—Power Systems)			Semester II		
	PP	TW		PP	TW	Oral	
Microprocessor & Applications in Power System	<input type="checkbox"/>	—	Power Systems Planning & Reliability	<input type="checkbox"/>	—	—	
Computer Application in Power System	<input type="checkbox"/>	—	Power Systems Dynamics	<input type="checkbox"/>	—	—	
Power System Modelling	<input type="checkbox"/>	—	Power System Protection	<input type="checkbox"/>	—	—	
High Voltage Power Transmissions	<input type="checkbox"/>	—	Power System Operation & Control	<input type="checkbox"/>	—	—	
Lab. Practice I	—	<input type="checkbox"/>	Elective	<input type="checkbox"/>	—	—	
			Lab. Practice II	—	<input type="checkbox"/>	—	
			Seminar I	—	<input type="checkbox"/>	—	
Semester III		PP	OR	Semester IV			
Seminar II	—	<input type="checkbox"/>	Seminar III	PP	TW	Oral	
			Dissertation	<input type="checkbox"/>	—	—	
				—	<input type="checkbox"/>	<input type="checkbox"/>	

**Electives I*

- (a) Special Topics in High Voltage Engg.
- (b) Power Sector Economics Regulation & Restructuring
- (c) Advanced Microprocessor & Application
- (d) Harmonics
- (e) Renewable energy System

M.E. (Electrical—Control Systems)

Semester I			Semester II			
	PP	TW		PP	TW	Oral
Digital Control Systems	<input type="checkbox"/>	—	Optimization Techniques & Control	<input type="checkbox"/>	—	—
Non Linear Control System	<input type="checkbox"/>	—	Microcontroller & its Applications in Control System	<input type="checkbox"/>	—	—
Multivariable Control System	<input type="checkbox"/>	—	Identification & Adoptive Control	<input type="checkbox"/>	—	—
Computer Methods in Control System	<input type="checkbox"/>	—	Intelligent Control	<input type="checkbox"/>	—	—
Lab. Practice I	—	<input type="checkbox"/>	Elective	<input type="checkbox"/>	—	—
			Lab. Practice II	—	<input type="checkbox"/>	—
			Seminar I	—	<input type="checkbox"/>	—
Semester III			Semester IV			
	PP	OR		PP	TW	Oral
Seminar II	—	<input type="checkbox"/>	Seminar III	—	<input type="checkbox"/>	—
			Dissertation	—	<input type="checkbox"/>	<input type="checkbox"/>

**Electives I*

- (a) Automation & Robotics
- (b) Digital Signal Processing
- (c) Process Control

M.E. (Electronics and Telecommunication (Instrumentation))

Semester I			Semester II			
	PP	TW		PP	TW	Oral
VLSI Design & Tech.	<input type="checkbox"/>	—	Intelligent Instrumentation	<input type="checkbox"/>	—	—
Advanced DSP	<input type="checkbox"/>	—	Embedded System Design	<input type="checkbox"/>	—	—
Digital Control Systems	<input type="checkbox"/>	—	Machine Vision Systems	<input type="checkbox"/>	—	—
Communication Networks	<input type="checkbox"/>	—	Elective I	<input type="checkbox"/>	—	—
Modern Electronic Design Practice I	—	<input type="checkbox"/>	Elective II	<input type="checkbox"/>	—	—
			Modern Electronic Design Practice II	—	<input type="checkbox"/>	—
			Seminar I	—	<input type="checkbox"/>	—
Semester III			Semester IV			
	PP	TW		PP	TW	Oral
Seminar II	—	<input type="checkbox"/>	Seminar III	—	<input type="checkbox"/>	—
			Dissertation	—	<input type="checkbox"/>	<input type="checkbox"/>

**Electives I*

- (a) Reconfigurable Computing
- (b) High Performance Networks

**Electives II*

- (c) Industrial drives & Control
- (d) Biomedical Instrumentation

[P.T.O.]

M.E. (Electronics and Telecommunication) (Microwave)

Semester I			Semester II			
	PP	TW		PP	TW	Oral
Statistical Signal Analysis and Stochastic Processes	<input type="checkbox"/>	—	RF & Microwave Circuit Design	<input type="checkbox"/>	—	—
Advanced DSP	<input type="checkbox"/>	—	Microelectronics	<input type="checkbox"/>	—	—
Antenna & Radiating Systems	<input type="checkbox"/>	—	Multiple Access	<input type="checkbox"/>	—	—
Communication Network	<input type="checkbox"/>	—	Communication Systems			
Modern Electronic Design Practice I	—	<input type="checkbox"/>	Elective I	<input type="checkbox"/>	—	—
			Elective II	<input type="checkbox"/>	—	—
			Modern Electronics Design Practice II	—	<input type="checkbox"/>	—
			Seminar I	—	<input type="checkbox"/>	—
Semester III			Semester IV			
	PP	TW		PP	TW	Oral
Seminar II	—	<input type="checkbox"/>	Seminar III (Dissertation)	—	<input type="checkbox"/>	—
				—	<input type="checkbox"/>	<input type="checkbox"/>
<i>*Electives I :</i>			<i>*Electives II :</i>			
(a) Machine Vision and Pattern Analysis			(c) Integrated Optics & Optical Communication			
(b) Network Security System			(d) Continuous Speech Processing			

M.E. (Electronics) (Digital Systems)

Semester I			Semester II			
	PP	TW		PP	TW	Oral
Microelectronics	<input type="checkbox"/>	—	Advanced Digital System Design	<input type="checkbox"/>	—	—
DSP and Applications	<input type="checkbox"/>	—	Multimedia Techniques	<input type="checkbox"/>	—	—
Embedded System Design	<input type="checkbox"/>	—	Network Security Systems	<input type="checkbox"/>	—	—
Computer Networks	<input type="checkbox"/>	—	Elective I	<input type="checkbox"/>	—	—
Modern Electronics Design Practice I	<input type="checkbox"/>	—	Elective II	<input type="checkbox"/>	—	—
			Modern Electronics Design Lab. Practice II	—	<input type="checkbox"/>	—
			Seminar I	—	<input type="checkbox"/>	—
Semester III			Semester IV			
	PP	TW		PP	TW	Oral
Seminar II	—	<input type="checkbox"/>	Seminar III (Dissertation) Dissertation	—	<input type="checkbox"/>	—
				—	<input type="checkbox"/>	<input type="checkbox"/>
<i>*Electives I :</i>			<i>*Electives II :</i>			
(a) Paraller Computing			(c) Reconfigurable Computing			
(b) Image Processing & Pattern Recognition			(d) Expert Systems			

M.E. (Electronics) (Computer)

Semester I			Semester II			
	PP	TW		PP	TW	Oral
Software Metrics	<input type="checkbox"/>	—	Parallel Processing Systems	<input type="checkbox"/>	—	—
DSP and Applications	<input type="checkbox"/>	—	Systems on Chip	<input type="checkbox"/>	—	—
Embedded System & Software	<input type="checkbox"/>	—	Multimedia Systems	<input type="checkbox"/>	—	—
Computer Network	<input type="checkbox"/>	—	Elective I	<input type="checkbox"/>	—	—
Modern Electronic Design	—	<input type="checkbox"/>	Elective II	<input type="checkbox"/>	—	—
Practice I			Modern Electronics Design	—	<input type="checkbox"/>	—
			Practice II Seminar I	—	<input type="checkbox"/>	—
Semester III			Semester IV			
Seminar II	—	<input type="checkbox"/>	Seminar III (Dissertation) Dissertation	—	<input type="checkbox"/>	—
				—	<input type="checkbox"/>	<input type="checkbox"/>
<i>*Electives I :</i>			<i>*Electives II :</i>			
(a) Artificial Intelligence & Expert Systems			(c) Compiler Construction			
(b) High Performance Computer Network			(d) Applied Algorithms			

M.E. (Physical) (Metallurgy)

Semester I			Semester II			
	PP	TW		PP	TW	Oral
Advanced Numerical Methods & Computational Instrumentation & Control	<input type="checkbox"/>	—	Phase Transformations of Metals and Alloys	<input type="checkbox"/>	—	—
Advanced Powder Technology	<input type="checkbox"/>	—	Science & Technology of Modern Materials	<input type="checkbox"/>	—	—
Advanced Metal Costing	<input type="checkbox"/>	—	X-Ray & Electron Metallography	<input type="checkbox"/>	—	—
Lab Practice I	—	<input type="checkbox"/>	Mechanical Properties of Metals & Alloys	<input type="checkbox"/>	—	—
			Surface Processing of Materials	<input type="checkbox"/>	—	—
			Laboratory Practice II Seminar I	—	<input type="checkbox"/>	—
				—	<input type="checkbox"/>	—
Semester III			Semester IV			
	TW	Oral		PP	TW	Oral
Seminar II	—	<input type="checkbox"/>	Seminar III Dissertation	—	<input type="checkbox"/>	—
				—	<input type="checkbox"/>	<input type="checkbox"/>

[P.T.O.]

M.E. (Process) (Metallurgy)

Semester I			Semester II			
	PP	TW		PP	TW	Oral
Advanced Numerical Methods & Computational Techniques	<input type="checkbox"/>	—	Phase Transformations of Metals and Alloys	<input type="checkbox"/>	—	—
Instrumentation & Control	<input type="checkbox"/>	—	Mechanical Working of Metals	<input type="checkbox"/>	—	—
Advanced Powder Technology	<input type="checkbox"/>	—	Special manufacturing Processes	<input type="checkbox"/>	—	—
Advanced Metal Casting	<input type="checkbox"/>	—	Joining of Materials and Investigations of Failures	<input type="checkbox"/>	—	—
Lab. Practice I	—	<input type="checkbox"/>	Advanced Experimental Techniques	<input type="checkbox"/>	—	—
			Laboratory Practice II	—	<input type="checkbox"/>	—
			Seminar I	—	<input type="checkbox"/>	—
Semester III			Semester IV			
Seminar II	—	<input type="checkbox"/>	Seminar III	—	<input type="checkbox"/>	—
			Dissertation	—	<input type="checkbox"/>	<input type="checkbox"/>